CHAPTER 6
BOOLEAN ALGEBRA

Q1. What is Boolean algebra?
Ans: In 1847, a British Mathematician, George Boole, introduced Algebra of Binary Numbers, known as Boolean algebra. Boolean algebra is used in designing of logic circuits, used in computer. These circuits perform different logical operations. Boolean algebra is also known as logical algebra or switching algebra.

Q6. Explain the following Logic gates and show their function by using a Truth Table. AND, OR, NOT
Ans: **AND Gate:** AND operation is represented by a dot. It is used for logical multiplication.

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**OR Gate:** OR operation is represented by a plus. It is used for logical addition.

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**Not Gate:** The NOT Gate is an inverter gate with only one input and one output signal.

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Q3. Find the values of the Boolean expressions
Ans: i) \(XY + X\overline{Y}\) when \(X=1\) and \(Y=0\)
\[= 1.0 + 1.1\]
\[= 0 + 1\]
\[= 1\]

ii) \((X+Y) \cdot (XY)\) when \(X=1\) and \(Y=0\)
\[= (1+0) \cdot (1.0)\]
\[= 1.0\]
\[= 0\]

iii) \((\overline{X} + \overline{Y}) \cdot (\overline{X} + \overline{Y})\) when \(X=1\) and \(Y=1\)
\[= (1+0) \cdot (0+1)\]
\[= 1.1\]
\[= 1\]

Q4. Find out the dual of the following Boolean expressions:
i). \(XY + X\overline{Y}\)
Solution: \((X+Y) \cdot (\overline{X} + \overline{Y})\)

ii). \((X + Y) + (X.Y)\)
Solution: \((\overline{X} \cdot Y) \cdot (X + Y)\)

iii) \((\overline{X} + \overline{Y}) + (\overline{X} + \overline{Y})\)
Solution: \((X \cdot Y) \cdot (\overline{X} \cdot Y)\)

Q5. State and prove the two basic Demorgan’s theorem.
Ans: Demorgan’s Law
It states that the complement of any expression is equal to the complement of each Boolean variable independently if the operators are changed i.e ‘+’ changes to ‘.’ and ‘.’ changes to ‘+’.
Q6. **What is a truth table?**
**Ans:** A truth table is a table that shows the result of a Boolean expression for all the possible combinations of the values. For example the truth table for OR operation is as follows:

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<th>OR OPERATION</th>
<th>A</th>
<th>B</th>
<th>X=A . B</th>
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Q7. **Construct a truth table for AND and Not of AND operation for the three variables X,Y and Z.**

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<th>Y</th>
<th>Z</th>
<th>X.Y.Z</th>
<th>NOT(X.Y.Z)</th>
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Q8. **State the following laws:**
**Ans:**
- **Idempotent Law**
  It states that sum or product of a variable with itself is equal to that variable i.e. \(A+A=A\) and \(A \cdot A=A\).
- **Absorption Law**
  (i) The sum of a Boolean variable with the product of another variable & itself is equal to the first variable. i.e. \(A+(A \cdot B)=A\).
  (ii) The product of a Boolean variable with the sum of another variable & itself is equal to the first variable. i.e. \(A \cdot (A+B)=A\)
- **Involution Law**
  It states that double complementation has cancellation effect. i.e. \(A=A\)

Q9. **Construct a truth table for the following Boolean expression.**
**Ans.**

\(i) \quad XY+X \bar{Z}+YZ\)

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<th>X</th>
<th>Y</th>
<th>Z</th>
<th>(\bar{X})</th>
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<th>X (\bar{Z})</th>
<th>YZ</th>
<th>XY+X (\bar{Z})+YZ</th>
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\(ii) \quad (\bar{X}+Y) \cdot (XY)\)

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iii) \( XY + XZ + Y\bar{Z} \)

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<th>XZ</th>
<th>Y( \bar{Z} )</th>
<th>X( \bar{Y} ) + XZ + Y( \bar{Z} )</th>
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Q11. Simplify the following Boolean expression.

i) \( \bar{A}C + \bar{A}B + A\bar{B}C + BC \)

\[
= \bar{A}C + \bar{A}B + A\bar{B}C + BC = \bar{A}C + \bar{A}B + BC + A\bar{B}C
\]

\[
= \bar{A}BC + \bar{A}BC + \bar{A}B + A\bar{B}C + BC = \bar{A}C + \bar{A}B + A(\bar{B} +AB)
\]

\[
= BC(1 + \bar{A}B) + \bar{A}BC + \bar{A}B + A\bar{B}C + AB
\]

\[
= BC \cdot (1 + \bar{A}B) + \bar{A}C + A\bar{B} + BC = \bar{A}C + A\bar{B} + C.(B + A)
\]

\[
= BC + B C + A\bar{B}
\]

\[
= C(B + \bar{B}) + A\bar{B} = C(A + \bar{A}) + A\bar{B} + BC
\]

\[
= C + A\bar{B}
\]

\[
\text{OR} \quad = C(1 + B) + A\bar{B}
\]

\[
= C + A\bar{B} = C + A\bar{B}
\]

ii) \( (A+B+C).(A+B +C).(A+B +C).(A +B +C) \)

\[
\text{LET} A+B = D, A+B = E
\]

\[
= (D+C).(E+\bar{C}).(D+\bar{C}).(E+C) = X.Y.(Z +\bar{Z}) + X.\bar{Y}.(Z +\bar{Z})
\]

\[
= (D+C).(E+\bar{C}).(D+\bar{C}).(E+C) = X.\bar{Y}.1 + X.\bar{Y}.1
\]

\[
= (D+C).(E+\bar{C}).(E+C) = X.\bar{Y} + X.\bar{Y} = \bar{Y} + \bar{Y}
\]

\[
= (D+0).(E+0)
\]

\[
= \bar{Y}.
\]

\[
= D . \bar{E}
\]

\[
= (A+B). (A+\bar{B})
\]

\[
= A+(B. \bar{B})
\]

\[
= A+0
\]

\[
= A
\]

Q12. Simplify the following function using karnaugh map.

i) \( \bar{A} \bar{B} \bar{C} + ABC + \bar{B} \bar{C} \)

\[
= \bar{A} \bar{B} \bar{C} + ABC + \bar{B} \bar{C} = \bar{A} \bar{B} \bar{C} + A \bar{B} \bar{C} + \bar{A} \bar{B} \bar{C}
\]

\[
= \bar{B} \bar{C} + AC
\]
ii) \[ ABC + A \overline{B} C + AB \overline{C} \]

\[
\begin{array}{c|c|c|c|c}
A & \overline{B} C & \overline{B} C & B C & B C \\
\hline
\overline{A} & 1 & 1 & 1 & \\
A & 1 & 1 & 1 & 1
\end{array}
\]

\[ = AC + AB \]
\[ = A(C + B) \]

iii) \[ \overline{A} \overline{B} C + \overline{A} B \overline{C} + \overline{A} B C \]

\[
\begin{array}{c|c|c|c|c}
A & \overline{B} C & \overline{B} C & B C & B \overline{C} \\
\hline
\overline{A} & 1 & 1 & 1 & 1 \\
A & 1 & 1 & 1 & 1
\end{array}
\]

\[ = \overline{B} C + \overline{A} B \overline{C} \]

iv) \[ \overline{A} \overline{B} + \overline{A} C + B \overline{C} \]

\[ = \overline{A} B (C + \overline{C}) + \overline{A} C (B + \overline{B}) + B \overline{C} (A + \overline{A}) \]

\[ = \overline{A} \overline{B} C + \overline{A} B \overline{C} + \overline{A} B C + A B C + \overline{A} B \overline{C} + A B \overline{C} \]

\[
\begin{array}{c|c|c|c|c|c}
A & \overline{B} C & \overline{B} C & B C & B \overline{C} \\
\hline
\overline{A} & 1 & 1 & 1 & 1 \\
A & 1 & 1 & 1 & 1
\end{array}
\]

\[ = \overline{A} \overline{B} + \overline{A} C + B \overline{C} \]

GENERAL QUESTIONS

Q1. What is Karnaugh map?
Ans: Karnaugh map are used to simplify the Boolean expression. It is used to minimize the number of literal and hence requires less number of gates for its implementation.

Q2. What is Boolean expression?
Ans: In Boolean expression, statements are represented by variables connected or operated by logical operators.

Q3. Prove with the help of truth table that
i). \[ A + \overline{B} = \overline{A} \cdot \overline{B} \]

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<th>B</th>
<th>A + B</th>
<th>\overline{A} + \overline{B}</th>
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ii) \[ A \cdot \overline{B} = \overline{A} + \overline{B} \]

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Q4: Define Logic Gate.
Ans: A Gate is a circuit that controls the flow of information performing logical operations.

Q5. Define the AND, OR and NOT Gates.
AND Gate: The AND Gate has two or more input signals and one output signal. It is used for AND operation.
OR Gate: The OR Gate has two or more input signals and one output signal. It is used for OR operation.
NOT Gate: The NOT Gate is an inverter gate with only one input signal and one output signal.